

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 1207**  
Katsuhiro NAKAI et al. : Attorney Docket No. 2004\_1946A  
Serial No. 10/517,258 : Group Art Unit 2136  
Filed December 7, 2004 : Examiner C. C. Okoronkwo  
  
SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE, DATA STORAGE  
VERIFICATION DEVICE, AND  
DATA STORAGE VERIFICATION  
METHOD

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action dated July 17, 2008, please amend the above-identified application as follows: